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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Tamitani, et al.

Serial No.: 10/030,525

I.A. Filed: May 02, 2000

Confirmation No.: 5906

For:

Semiconductor Device And

Method Of Manufacturing A

Semiconductor Device

Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Group Art Unit: 1765

Examiner:

Unknown

CERTIFICATE OF MAILING

37 CFR 1.8

I hereby certify that this correspondence is being deposited on November _____, 2002 with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231-

Sianature

REQUEST FOR CORRECTION OF ERROR ON FILING RECEIPT

The Filing Receipt for the above-referenced application contains an error regarding the Applicant(s) section. The Filing Receipt recites the fourth Applicant's residence as [Narita-shi, JAPAN.] Please delete [Narita-shi, JAPAN] and insert <u>SINGAPORE</u>. The fourth line of the Applicant(s) section should read:

Seayoul Park, SINGAPORE;

Attached is a copy of the Filing Receipt with the above change noted thereon. Please correct this error or notify the applicants of the reasons for denying the request.

Respectfully submitted,

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Washington, DC 20231

 APPLICATION NUMBER
 FILING DATE
 GRP ART UNIT
 FIL FEE REC'D
 ATTY.DOCKET.NO
 DRAWINGS
 TOT CLAIMS
 IND CLAIMS

 10/030,525
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 1765
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CONFIRMATION NO. 5906

Moser Patterson & Sherdian Suite 1500 3040 Post Oak Boulevard Houston, TX 77056 Date Mailed: 10/15/2002

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Applicant(s)

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Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP00/02914 05/02/2000

Foreign Applications

Pr jected Publication Date: None, application is not eligible for pre-grant publication

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device and method of manufacturing semiconductor device

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